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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT Gregory S. SNIDER	
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U.S. PATENT DOCUMENTS

Examiner Initials	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication (MM-DD-YYYY)
	Number	Kind Code (if known)		

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FOREIGN PATENT DOCUMENTS

Examiner Initials	Foreign Patent Document		Country	Date of Publication (MM-DD-YYYY)	Translation	
	Number	Kind Code (if known)			Yes	no

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	1. VAUGHN BETZ ET AL.; Architecture and CAD for Deep-Submicron FPGAs; Routing Tools and Routing Architecture Generation; Chapter 4; pages 68-70; Kluwer Academic Publishers; Boston/Dordrecht/London, 1999.
	2. LARRY MCMURCHIE ET AL.; Pathfinder: A Negotiation Based Performance-Driven Router for FPGAs; 7 pages; Department of Computer Science and Engineering, University of Washington, Seattle, Washington, Proceedings of 1995 ACM Third International Symposium on Field Programmable Gate Arrays, Feb. 1995, pp. 111-117.

Examiner Signature		Date Considered	7/9/2003